

Resume

Mrs. Sailakshmi Kumari Narava

Email: nslkumari@lbce.edu.in

Mobile No: 7287918971



Permanent Address:

Flat No C9,
Srinivasa Paradise
P.M.Palem
Visakhapatnam

Present Address

Flat No C9,
Srinivasa Paradise
P.M.Palem
Visakhapatnam

Objective:

To work for leading, dynamic organization and to provide best of my abilities and skills to establish myself as an efficient team player in a positive learning environment and delivering to my maximum potential.

Area of interest:

- Teaching, Research & Development

Educational qualifications:

Qualification	Institution	University/Board	Passing Year	Percentage
Ph.D (Wireless communications)	Sathyabama Institute of science and technology deemed to be university, Chennai	Anna university	Expecting in 2025	Pursuing(Pre-PhD completed)
M.Tech (VLSI)	Pydah Institute of Engineering and Technology, Visakhapatnam.	Jawaharlal Nehru Technological University Kakinada	2012	73%
B.E(ECE)	Sankethika Vidya Parishad, Engg College. Visakhapatnam.	Andhra University	2009	69%

XII	Sri Chaitanya Junior College, Visakhapatnam	Board of Intermediate Education	2005	75.9%
S.S.C	Sree Siddhartha Residential School, Rajahmundry	Board of Secondary Education	2003	77%

Workshops & Conferences Attended:

- Attended a national seminar in VLSI Design conducted by ANDHRA UNIVERSITY in the year 2010.
- Presented a paper on “The Application of GPS in space craft navigation” at GITAM UNIVERSITY in the year 2008.
- Presented a paper on “Antennas in wireless communication” at SVP ENGG COLLEGE in the year 2008.
- Participated in Workshop on “Embedded Systems and RTOS Concepts”, conducted by ECE dept of Welfare Engg College in the year 2011.
- Participated in 1 week Faculty Development Programme in “5G Wireless Communication” in ANITS Engg College, which was conducted by NIT Warangal in January 2018.

Professional Experience & Training:

- Had overall 12 years of teaching experience as an Asst. Professor in various Engg Colleges
- Presently working as an Asst. Professor in the Dr. Lankapalli Bullayya College of Engg since August 2023
- Had 7 years 8 months of Teaching Experience as an Assistant Professor in Welfare Institute of Science Technology and Management (WISTM), Pendurthi, Visakhapatnam.
- Worked as an Assistant Professor in Malla Reddy Engineering College for Women,

Subjects Handled:

- Microwave Engineering
- Fiber Optical Communication
- Antennas and Wave Propagation
- Digital Communication
- Industrial Electronics
- Linear Integrated Circuits
- Digital Logic Design
- Television and Satellite Communication
- Analog electronic circuits
- Radar and navigational aids
- Linear and Digital Integrated Circuits

Laboratories Handled:

- Digital IC' s Lab
- Analog Communication Lab
- Digital Communication Lab
- Microprocessor-1 Lab
- DE and MP Lab
- Electronic Devices and Circuits Lab
- Analog Electronic Circuits Lab

➤ Projects guided: Under-graduate Total:10

- IOT based home automation
- Design and Development of a modern vigilance control system to enhance the rail passenger safety using GSM&GPS technology
- FPGA based Implementation of 32-bit RISC processor
- To improve register file integrity against soft errors by using self immunity technique
- Implementation of High Speed DMA system using VHDL

PHD RESEARCH AREA: Wireless Communications Domain Title: INVESTIGATIONS ON CFO ESTIMATION AND ICI MITIGATION IN MASSIVE MIMO MULTI CARRIER OFDM SYSTEM

Project done in my M.Tech:**TITLE: “Implementation of 2D Hartley Transform Using Distributive Arithmetic**

Abstract: Discrete Cosine Transform is usually used in JPEG based image transform coding. This paper presents a separable 2-D discrete Hartley transform and its distributed arithmetic-based hardware architecture as an alternate to DCT in transform-based coding of image compression. The proposed DA architecture for 1-D DHT has very less computations as compared to existing 1-D DCT. The additional advantage of SDHT is that its inverse transform is same as a forward transform with a constant division. This is demonstrated using a Xilinx FPGA XC3s500E device.

Project done in my B.E:

TITLE: “Automatic Reservoir Gate Control System”.

Journals Published :

1. Published a paper in TOJQI on “A Survey on Carrier Frequency Offset and Inter Carrier Interference Cancellation in MIMO OFDM Systems. Volume 12, Issue 10, October 2021: 969-978
2. Published a paper in IJITEE, Volume 3, Issue 5 in October 2012 on “Implementation of 2D Hartley Transform Using Distributed Arithmetic”

Strengths:

- Committed, hardworking and highly motivated to work under challenging situations.
- Open to new and innovative ideas and always want to learn more.
- Ability to work as a team member with a well developed sense of responsibility and cooperation.
- Ability to take on additional job responsibilities by working with boundary less approach.

Personal Profile:

Mother's Name : J. Neelaveni
Husband's Name : T. Laxmana Rao
Date of Birth : 26/06/1988.
Sex : Female.
Marital Status : Married
Languages Known : English, Telugu.

Information given above is correct to the best of my knowledge.

[**Sai Lakshmi Kumari Narava**]