

## CURRICULUM VITAE

Contact Address	Date of Birth	11 <sup>th</sup> April,1991
<b>Kandrapu Rajesh Kumar</b> D.No:4-16-17, Kandrapu vari street, Peda Waltair, Visakhapatnam Dt, Andhra Pradesh, 530017.	Contact no:	+919701969560
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### CAREER OBJECTIVE

To achieve a challenging position in dynamic organization providing the opportunity to make strong contribution to organizational goals, to excel and succeed through hard work, thus enhancing value addition to the organization, and my professional skills.

### ACADEMIC QUALIFICATIONS

- Pursuing Ph.D in Andhra University.
- M.Tech (**Radar & Microwave Engineering**) from A.U. College of Engg, Visakhapatnam in 2014.
- B.Tech (**Electronics & Communications Engineering**) from Jawaharlal Nehru Technological University Kakinada in 2012.
- 10+2 (**Intermediate**) in M.P.C stream from Mega Junior college Visakhapatnam affiliated to Board of Intermediate Education, A.P. in 2008.
- 10th (**S.S.C**) from Kotak salesian school Visakhapatnam affiliated to State Board of Secondary Education,A.P in 2006.

### PROFESSIONAL EXPERIENCE:

- Working as Assistant Professor in Dr.L.Bullayya College of engineering for women, Resapuvanipalem, Visakhapatnam from July 2015 to till date.

### SUBJECTS TAUGHT:

- Pulse and Digital circuits.
- Analog Electronics circuits.
- Basic electronics engineering.
- Analog communications.

- Digital Signal Processing.
- Global Positioning System.
- Digital communications.
- VLSI

## **ACCOMPLISHMENTS**

- Published International Journal Paper on “**Fabrication Model Design and Analysis of Flexible Polarization Surface Polariton Resonance with a Dual-Wing Antenna Structure Platform for Diverse Multiband Characteristics in the Measurement Environment**” in Springer, December 2023.
- Published Journal Paper on “**Design of Linear Antenna Array for Smart Antenna using Whale Optimization Algorithm**” in JETIR, Volume 8, Issue 10, August 2021.
- Published International Journal Paper on “**Design of Linear and Circular Antenna Array for Side Lobe Reduction Using the Method Moth Flame Optimization Algorithm**” in International Journal of Scientific Engineering and Applied Science (IJSEAS), Volume-6, Issue-9, September 2020, ISSN: 2395-3470.
- Published International Journal Paper on “**Side Lobe Reduction of a Concentric Circular Antenna Array using Genetic Algorithm (GA) and Particle Swarm Optimization (PSO)**” in International Journal of Engineering and Innovative Technology (IJEIT), Volume 4, Issue 5, November 2014.
- Successfully Completed 12-week Online Certification course on **DIGITAL CIRCUITS** With a consolidated score of 55% conducted by NPTEL, IIT Khargpur.
- Successfully Completed 12-week Faculty development programme on **MODERN DIGITAL COMMUNICATION TECHNIQUES** with a consolidated score of 70% conducted by NPTEL, IIT Madras.
- Participated in 5-day online FDP on “**Recent Trends in VLSI and Embedded Auto Industry**” Organized by **DEPT OF ECE, CMR Technical Campus**. 20 - 24 May, 2020.
- Participated in 3-day FDP on “**Faculty Development Program for Student Induction (FDP-SI)**” Organized by **All India Council for Technical Education (AICTE)**. 19 - 21 December, 2019 at G Pulla Reddy Engineering College, Kurnool.
- Participated in 2-day Workshop on “**GRAPHICAL SYSTEM DESIGN USING LABVIEW (NWLABVIEW-2K17)**” Organized by Conducted by ECE Dept, AU college of Engineering. 30<sup>th</sup> to 31<sup>st</sup> March. 2017.

- Participated in 3-day Workshop on “**National Workshop on Advance Antenna Design** ” Organized by Conducted by ECE Dept, AU college of Engineering. 19<sup>th</sup> to 21<sup>th</sup> October,2016.
- Participated in one-week online FDP on “**Enhancing faculty expertise: communication and Signal/Image processing (EFEC SIP-2023)**” Organized by department Electronics and Communication Engineering, Aditya College of Engineering & Technology from 10<sup>th</sup> to 15<sup>th</sup> July, 2023.
- Participated in 30 Hours online international FDP on “**Deep Learning for NLP and Computer Vision**” Organized by Chaitanya Bharathi Institute of Technology (CBIT) in Collaboration with ExcelR. From 10<sup>th</sup> July to 28<sup>th</sup> July 2023.
- Participated in 5-day Workshop on “**VLSI to System Design: Silicon To End Application Approach**” Organized by All India Council for Technical Education (AICTE), Arm Education and STMicroelectronics from July 31<sup>st</sup> to August 4<sup>th</sup> 2023.

### **STRENGTHS**

- Positive attitude
- Hardworking
- Team Player
- Always takes the initiative

### **CO-CURRICULAR ACTIVITIES**

- Represented my school in the district level cricket competitions.
- Served as a CADET in the field of NCC under AIR WING AND ARMY WING.
- Attended Combined Annual Training Camp where more than 3000 students all over Andhra Pradesh came from different wings and participated.

### **PERSONAL PROFILE**

Name : Kandrapu Rajesh Kumar  
 Father’s name : K.Venkata Rao  
 Mother’s name : K.Nagamani  
 Nationality : Indian  
 Languages Known : Telugu, English and Hindi  
 Hobbies : Playing cricket, Football, Spending time with children.

## **DECLARATION**

I hereby declare that all the information furnished above is true to the best of my knowledge and brief

Station: Visakhapatnam

Date:

**KANDRAPU RAJESH KUMAR**