



Dr. Lankapalli Bullayya College of Engineering

New Resapuvani Palem, Visakhapatnam

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Research papers Published

Faculty Name: Dr.K. Anuradha

Department: CSE

INTERNATIONAL JOURNALS:

1. K.Lokesh Krishna, Yahya Mohammed A.A.N, and K.Anuradha, “A High Speed Two Step Flash ADC” **Springer: Communications in Computer and Information Science (CCIS)**, Vol. 837, pp. (826-836) September 2018. **(UGC Care list –Group A & Scopus Indexed)**.
2. K.Lokesh Krishna, K.Anuradha and A.M.Murshed, “A 6-bit Low Power SAR ADC” **Springer: Communications in Computer and Information Science (CCIS)**, Vol. 839, pp. (642-652) October 2018. **(UGC Care list –Group A & Scopus Indexed)**.
3. M. Ummay Hagera, K. Lokesh Krishna & K. Anuradha, “Implementation of Smart Home Automation with Enhanced Security” *International Journal of Scientific Research in Science, Engineering and Technology*, Vol.4, Issue.4, pp. (528-532), March-April 2018. **(old UGC Approved Journal No: 47147)**.
4. U.S.Harshanga Kalingage, K. Lokesh Krishna & K. Anuradha, “Implementation of High Speed Double Tail Comparator” *International Journal of Scientific Research in Science and Technology*, Vol.4, Issue.5, pp.

(1064-1068), March-April 2018. **(old UGC Approved Journal No: 64011).**

5. K.Lokesh Krishna, G.Abhishek and K.Anuradha, "Design and Implementation of High Speed Low Power 12-bit Pipelined ADC," Annual Technical Volume of Electronics and Telecommunication Engineering Division Board, The Institution of Engineers (India), Vol.1 pp. (24-29), 2016.
6. V. Nanda Pramodh, K.Lokesh Krishna and K.Anuradha, "Detection of Shadows in Panchromatic Images Using Otsu's Thresholding" SSRG International Journal of Electronics and Communication Engineering (SSRG-IJECE), Vol.2, No.5, pp. (17-22), May 2015.
7. K. Deepika, K.Lokesh Krishna and K. Anuradha, "A Low power High speed Double Tail Comparator in 90nm CMOS Technology" International Journal of Computer Science Trends and Technology, Vol. 3, No. 2, pp. (203-207), April 2015.
8. B. Silpa, K.Lokesh Krishna and K. Anuradha, "Verilog Implementation of UART with Status Register" International Journal of Advanced Research in Computer Science and Software Engineering, Vol. 4, Issue.8, August 2014.
9. P.Rajeswari, K.Lokesh Krishna and K.Anuradha, "Design of an Autonomous Speech Recognition Humanoid," *International Journal of Research in Information Technology*, Vol. 2, No. 8, pp. (227-233), August 2014.
10. Naresh Prasad Das, K. Lokesh Krishna and K.Anuradha, "A QoS Based Proactive VHOM Scheme for Interworking of Wi-MAX/WLAN Networks" International Journal of Innovative Research in Computer and Communication Engineering, Vol.2, No.7, July 2014.
11. K. Lokesh Krishna, M. Jagadeesh, T.Ramashri, and K.Anuradha, "Design and Implementation of a Low Power Second Order Sigma-Delta ADC" *International Journal of Computer Science & Information Technologies*, Vol. 5, No. 5, pp (6044- 6049), (2014).